

FIG. 5

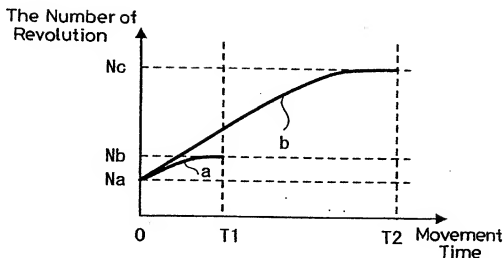


FIG. 6

PRIOR ART

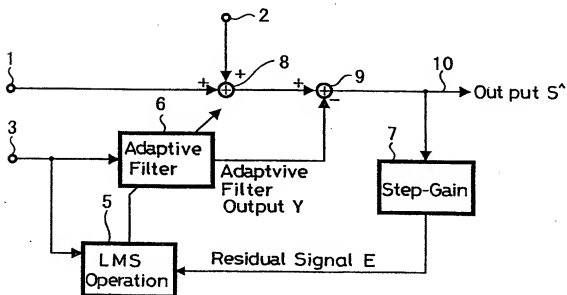


FIG. 7

PRIOR ART

6

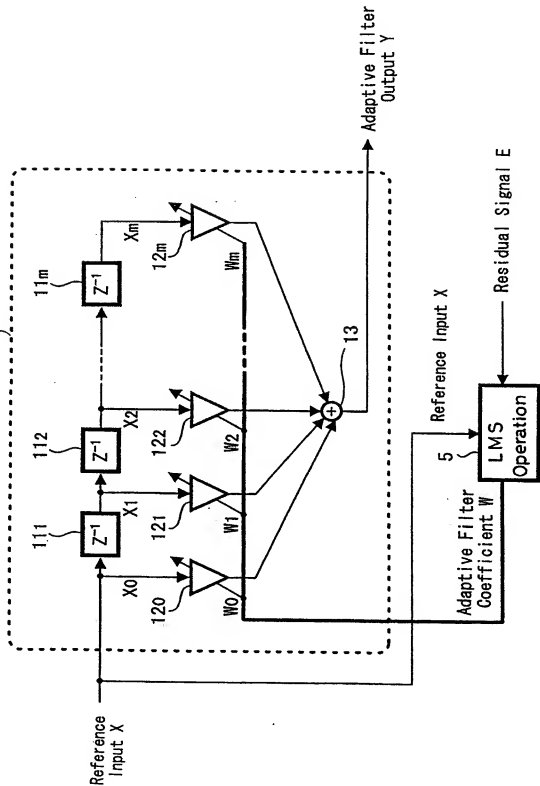


FIG. 8

PRIOR ART

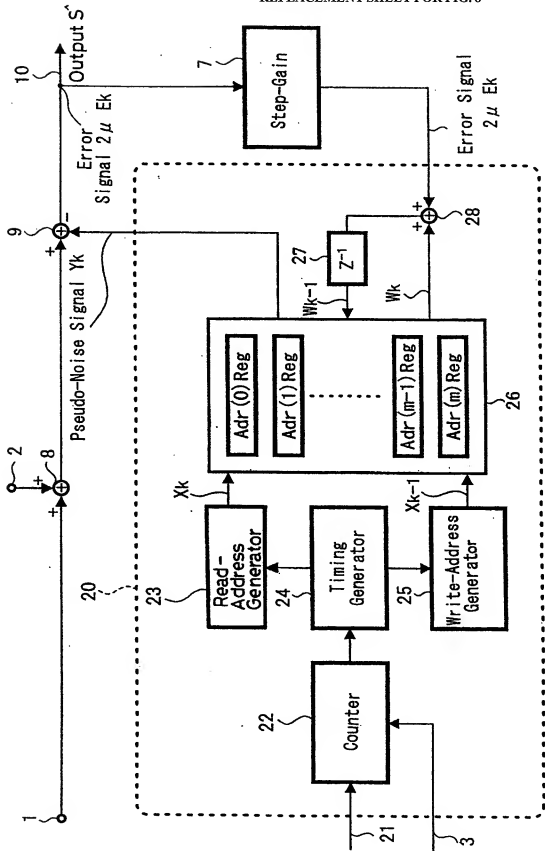
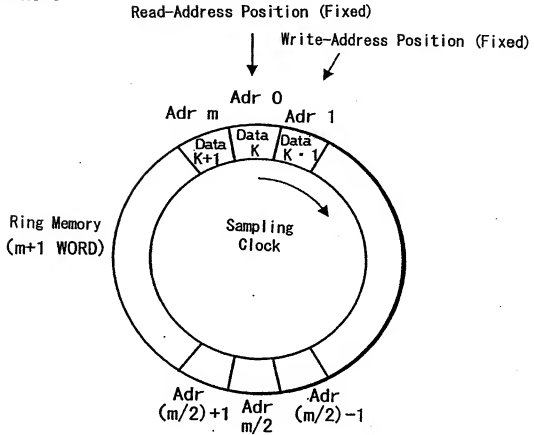


FIG. 9

PRIOR ART



PRIOR ART

FIG. 10A

Write Signal

PRIOR ART

FIG. 10B

Read Signal

